IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Epstein, Barry

Examiner: D. Nguyen

Serial No: 09/934,047

Art Unit: 2836

Filed: 08/22/01

Title: SYSTEM FOR PROTECTING A PERSON

FROM THE EFFECTS OF ESD

Hon. Commissioner of Patents and Trademarks Washington, D.C. 20231

FAX RECEIVED

JUN 1 2 2003

sir:

PROPOSED AGENDA FOR INTERVIEW WITH TECHNOLOGY CENTER 2800 EXAMINER ON 13 JUNE 2003

- A. Discussion of inventor's recognition of rapid rise time in the context of ESD and how critical it is to upsetting system performance of sensitive electronics when injected into a power ground, power mains or neutral, LAN or analog network.
- (1) discussion of what happens in the real world with leads, etc. and how this relates to the solution of the ESD problem as claimed in this application
- (2) discussion of resistances and impedance
- (3) discussion of impedance with inductance
- (4) discussion of the definition of "series" (current at any point in a series circuit will be the same and with respect to any element that is connected in series with another element current applied to both series connected elements will be the same for both elements); show how parallel is different.
- B. Discussion of claims.
- C. Discussion of references and their relevance to the claims of the application, and the way the references were applied in the Office Action:
 - (1) Lewis
 - (2) Ker
 - (3) Gabara
 - (4) Reinhardt
 - (5) Danglemayer
 - (6) Esper
 - (7) Johnson